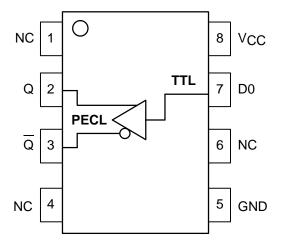
TTL to Differential PECL Translator

The MC10ELT/100ELT20 is a TTL to differential PECL translator. Because PECL (Positive ECL) levels are used only +5V and ground are required. The small outline 8-lead SOIC package and the single gate of the ELT20 makes it ideal for those applications where space, performance and low power are at a premium. Because the mature MOSAIC 1.5 process is used, low cost can be added to the list of features.

The ELT20 is available in both ECL standards: the 10ELT is compatible with positive MECL 10H logic levels while the 100ELT is compatible with positive ECL 100K logic levels.

- 1.5ns Typical Propagation Delay
- Differential PECL Outputs
- Small Outline SOIC Package
- · PNP TTL Inputs for Minimal Loading
- Flow Through Pinouts

LOGIC DIAGRAM AND PINOUT ASSIGNMENT



MC10ELT20



PIN DESCRIPTION

PIN	FUNCTION
Q	Diff PECL Outputs
D	TTL Input
VCC	+5.0V Supply
GND	Ground

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
Vcc	DC Supply Voltage (Referenced to GND)	7.0	V
V _{IN}	Input Voltage	0 to V _{CC}	V
lout	Current Applied to Output in Low Output State Continuou Surg		mA
TA	Operating Temperature Range (In Free-Air)	-40 to 85	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

^{*} Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

TTL INPUT DC CHARACTERISTICS (V_{CC} = 4.75V to 5.25V; T_A = -40°C to 85°C)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
lн	Input HIGH Current			20	μΑ	V _{IN} = 2.7V
Інн	Input HIGH Current			100	μΑ	V _{IN} = 7.0V
IIL	Input LOW Current			-0.6	mA	V _{IN} = 0.5V
VIK				-1.2	V	I _{IN} = -18mA
VIH	Input HIGH Voltage	2.0			V	
V _{IL}	Input LOW Voltage			0.8	V	

PECL OUTPUT DC CHARACTERISTICS ($V_{CC} = 4.75V \text{ to } 5.25V; T_A = -40^{\circ}\text{C to } 85^{\circ}\text{C}$)

		-40)°C	0 °	С		25°C		85	°C		
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
VOH	Output HIGH 10ELT ¹ Voltage 100ELT ¹	3.920 3.915	4.11 4.12	3.980 3.975	4.16 4.12	4.020 3.975	4.10 4.05	4.19 4.12	4.080 3.975	4.27 4.12	V	V _{CC} = 5.0V
VOL	Output LOW 10ELT1 Voltage 100ELT1	3.05 3.17	3.350 3.445	3.05 3.19	3.37 3.38	3.05 3.19	3.25 3.30	3.37 3.38	3.05 3.19	3.40 3.35	V	V _{CC} = 5.0V
Icc	Power Supply Current		16		16			16		16	mA	

^{1.} Levels will vary 1:1 with V_{CC}.

AC CHARACTERISTICS (V_{CC} = 4.75V to 5.25V; T_A = -40°C to 85°C)

		-40)°C	0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
tPLH	Propagation Delay ¹	0.6	1.2	0.65	1.45	0.9	1.2	1.5	0.6	1.35	ns	
tPHL	Propagation Delay ¹	0.4	1.0	0.45	1.05	0.5	0.8	1.1	0.7	1.30	ns	
t _r /t _f	Output Rise/Fall Time	0.15	1.5	0.15	1.5	0.15		1.5	0.15	1.5	ns	20–80%
fMAX	Maximum Input Frequency	100		100		100			100		MHz	

^{1.} Specifications for standard TTL input signal.

MOTOROLA 3–2

OUTLINE DIMENSIONS

D SUFFIX PLASTIC SOIC PACKAGE CASE 751–05 ISSUE P SEATING PLANE 0.25 (0.010) W T B S A S

NOTES:

- DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- 3. DIMENSIONS ARE IN MILLIMETER.
- DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
- 5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE. 6. DIMENSION D DOES NOT INCLUDE MOLD
- DIMENSION D DOES NOT INCLUDE MOLD PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS							
DIM	MIN	MAX						
Α	4.80	5.00						
В	3.80	4.00						
С	1.35	1.75						
D	0.35	0.49						
F	0.40	1.25						
G	1.27	BSC						
J	0.18	0.25						
K	0.10	0.25						
M	0 °	7 °						
Р	5.80	6.20						
R	0.25	0.50						

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